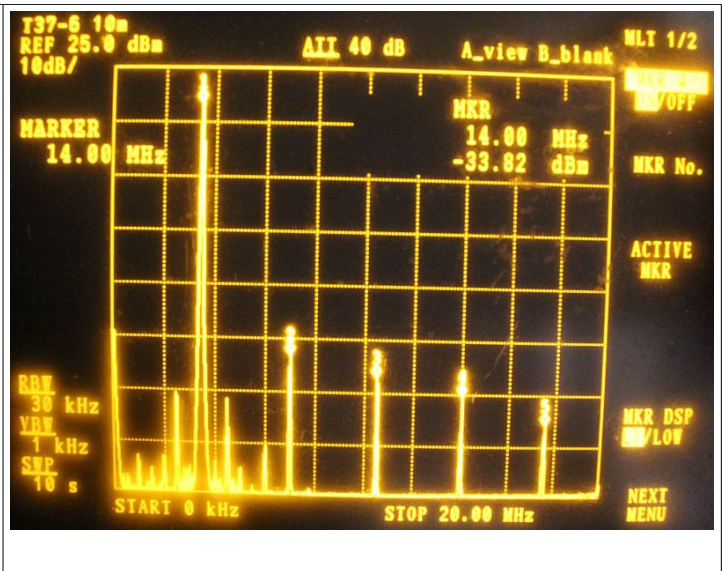
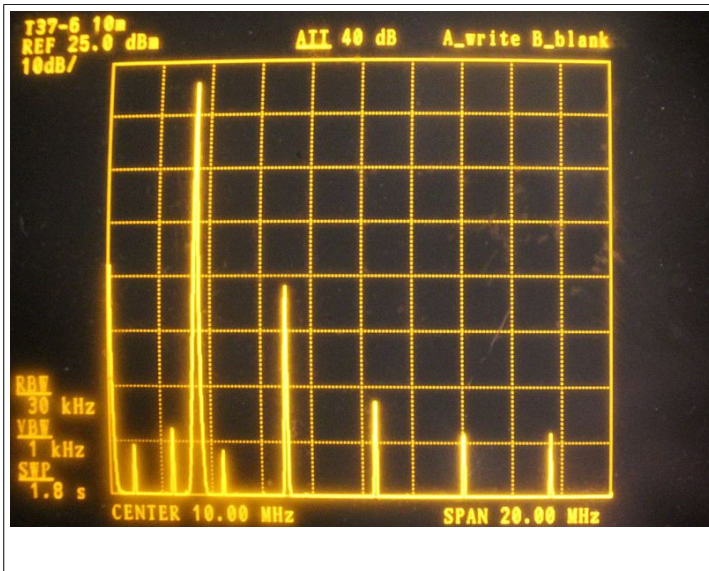
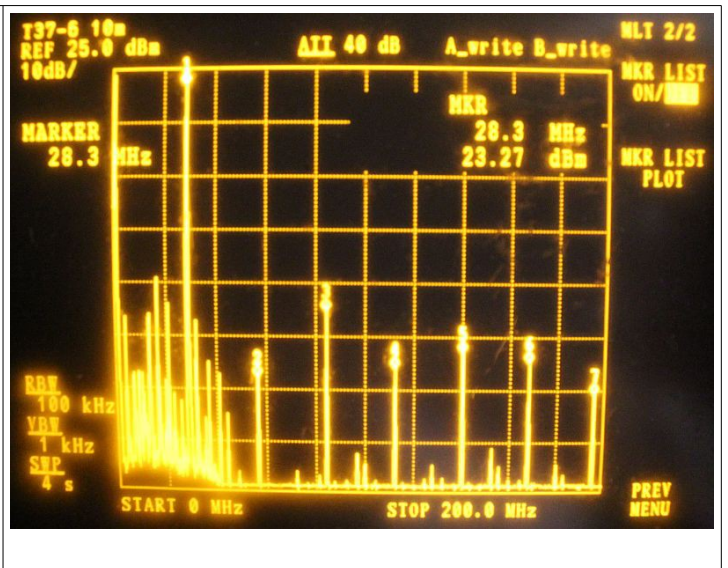
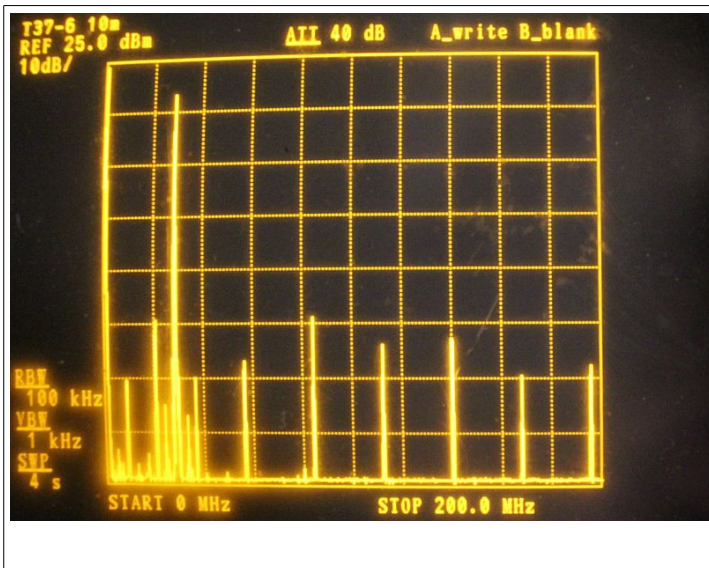


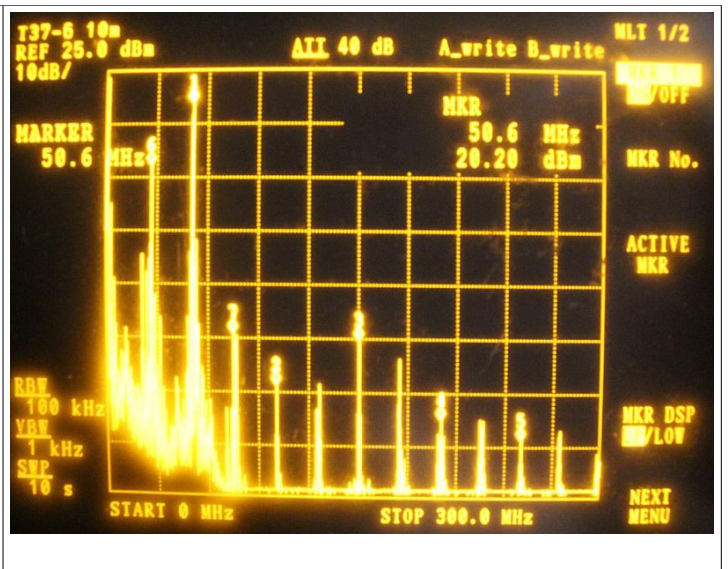
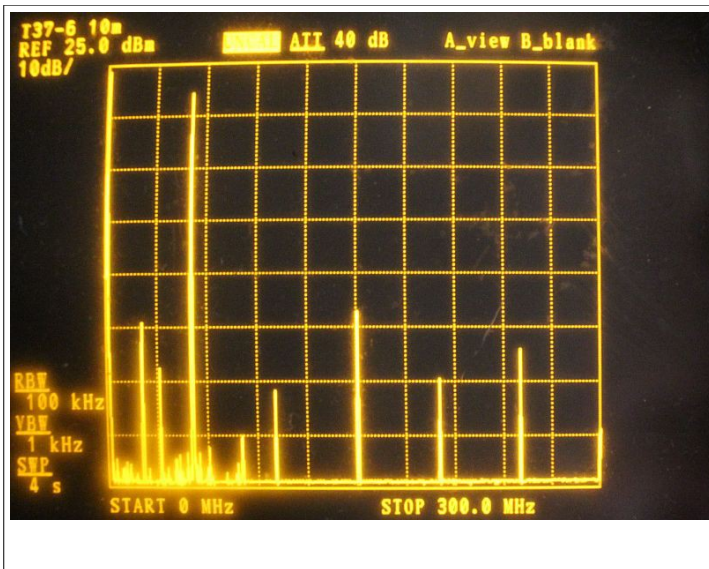
### 80m Si5351A vs AD9850



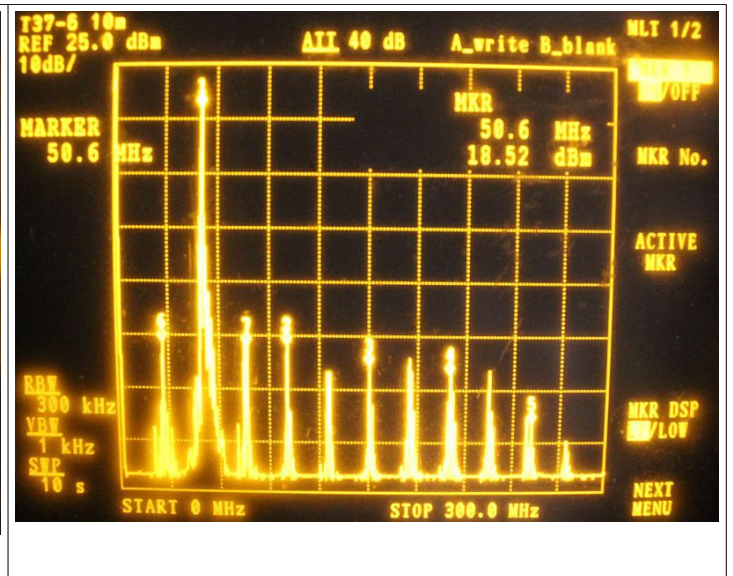
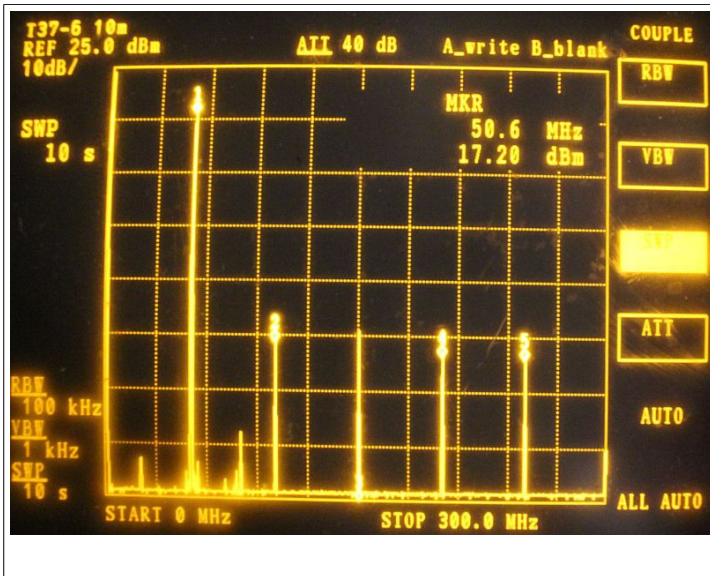
### 10m Si5351A vs AD9850



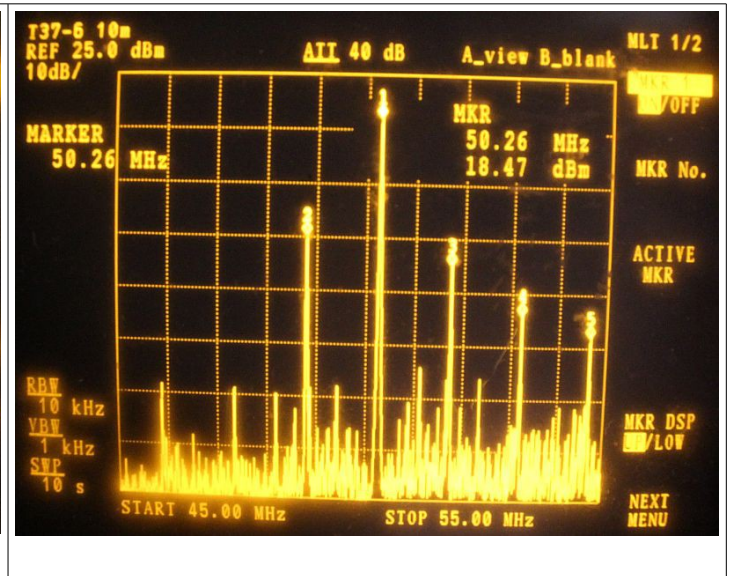
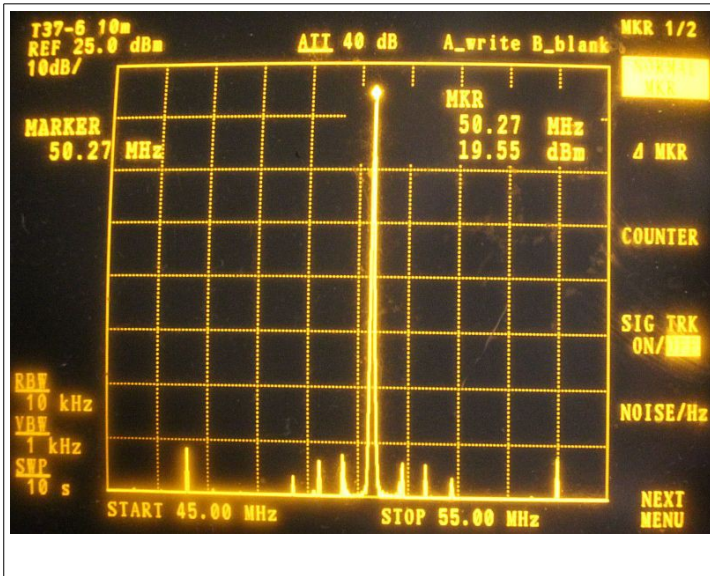
### 6m Si5351A vs AD9850 (using LPF)



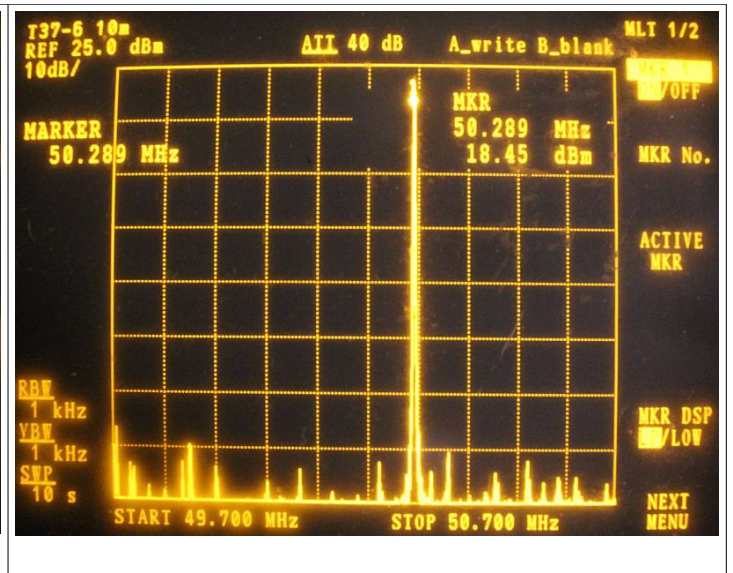
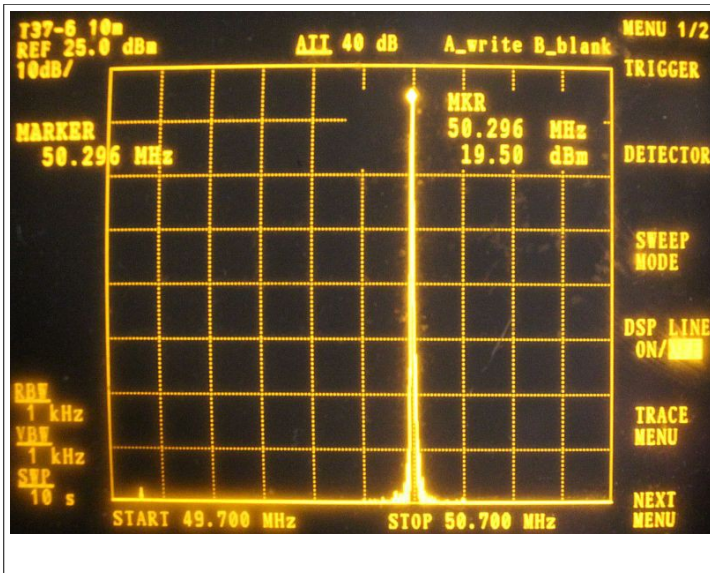
**6m Si5351A vs AD9850 using BPF**



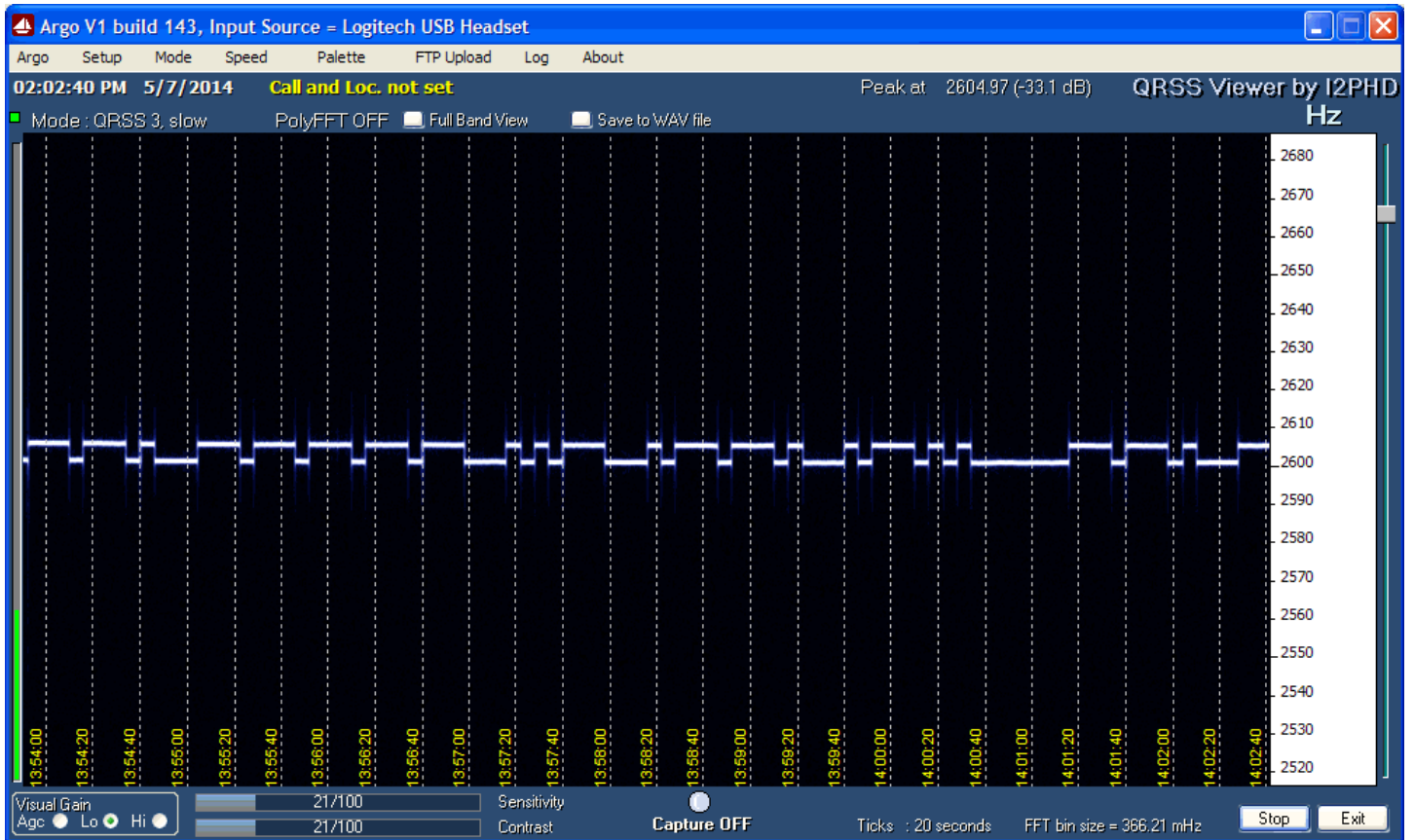
**6m 10MHz close-up, Si5351A (using LPF) vs AD9850 (using BPF)**



**6m 1MHz close-up, Si5351A (using LPF) vs AD9850 (using BPF)**



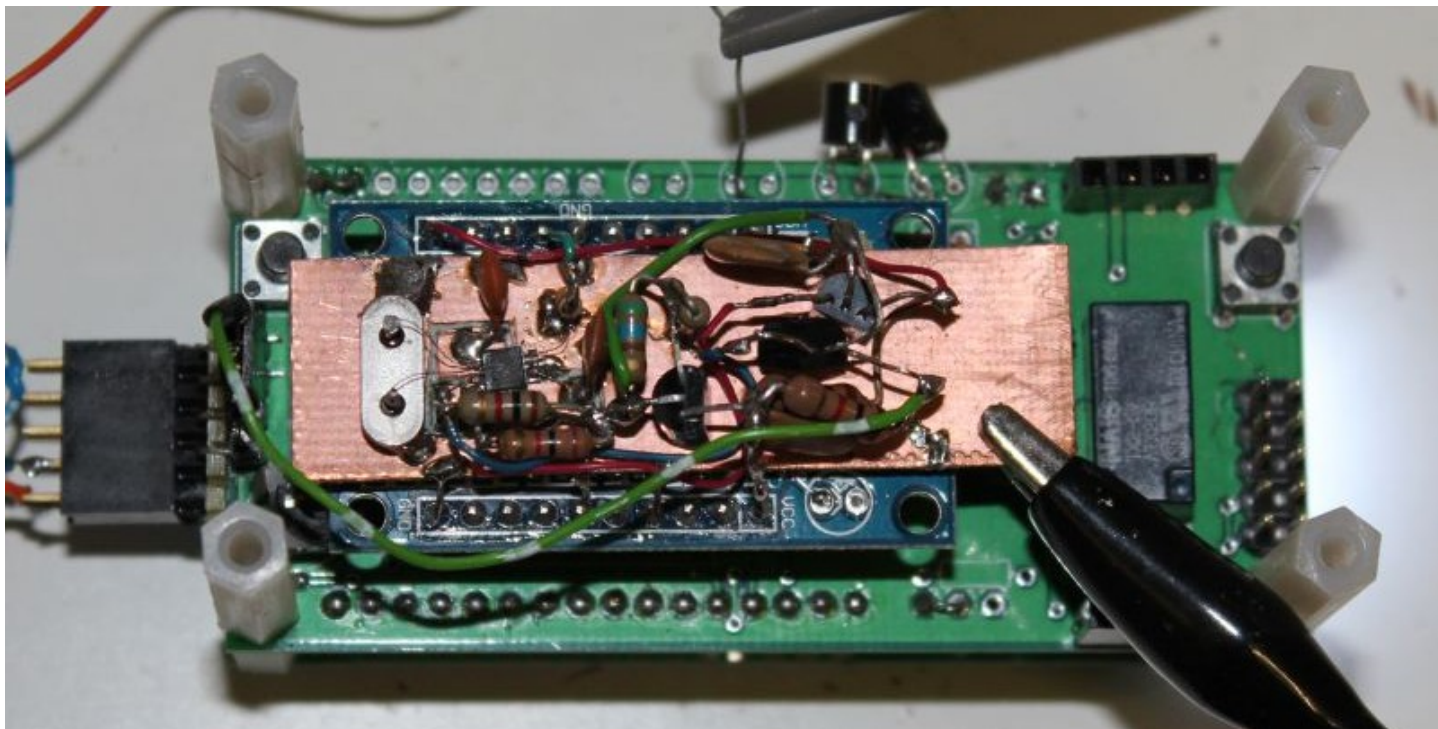
## U3 with new SI5351A module



## Side-by-side, SI5351A module (left) and AD9850 module (right)



U3 with new Si5351A module (uses stripped down AD9850 board as a holder)



Close-up of Si5351A chip ugly-style

